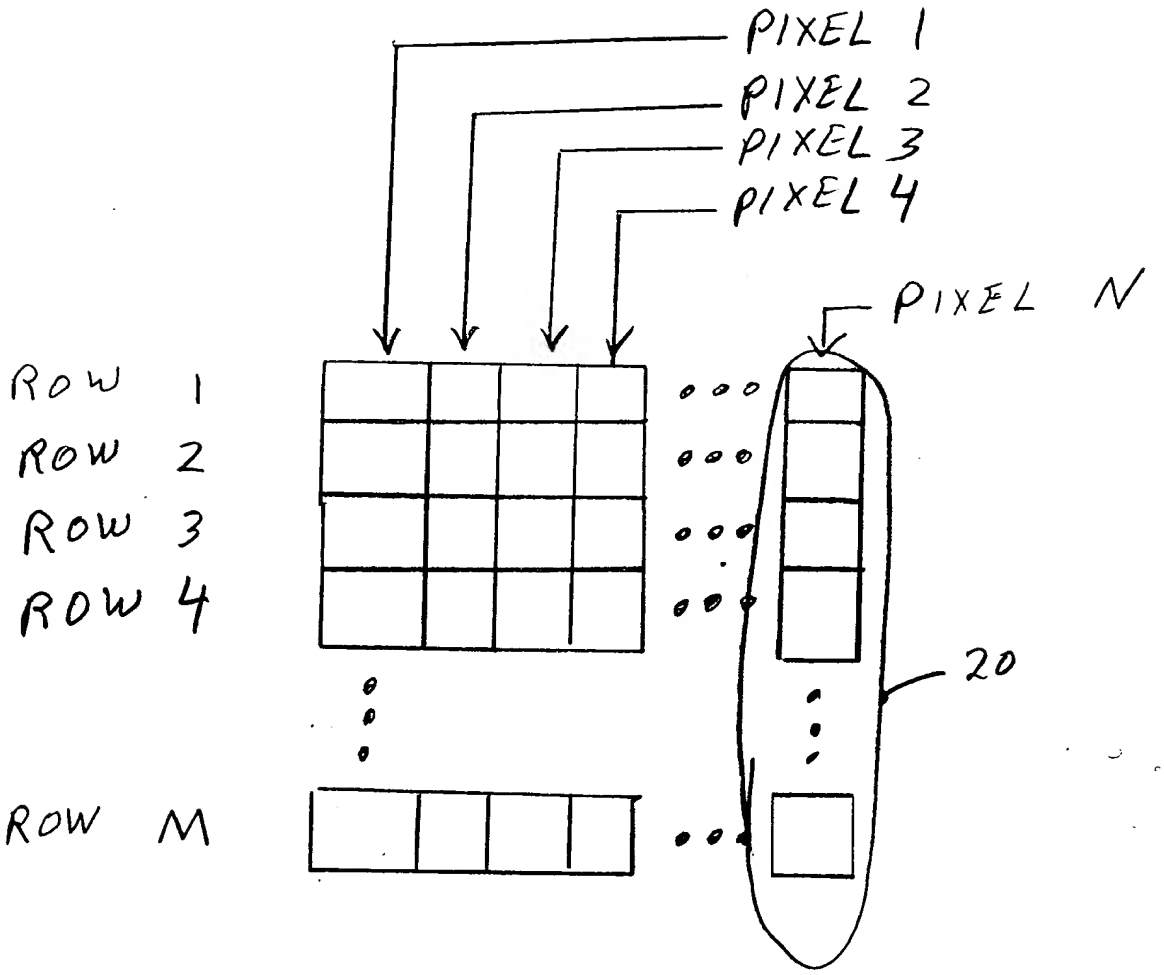

PIXEL 1
PIXEL 2
PIXEL 3
PIXEL 4
PIXEL N
ROW 1
ROW 2
ROW 3
ROW 4
ROW M
20

FIG. 1

10

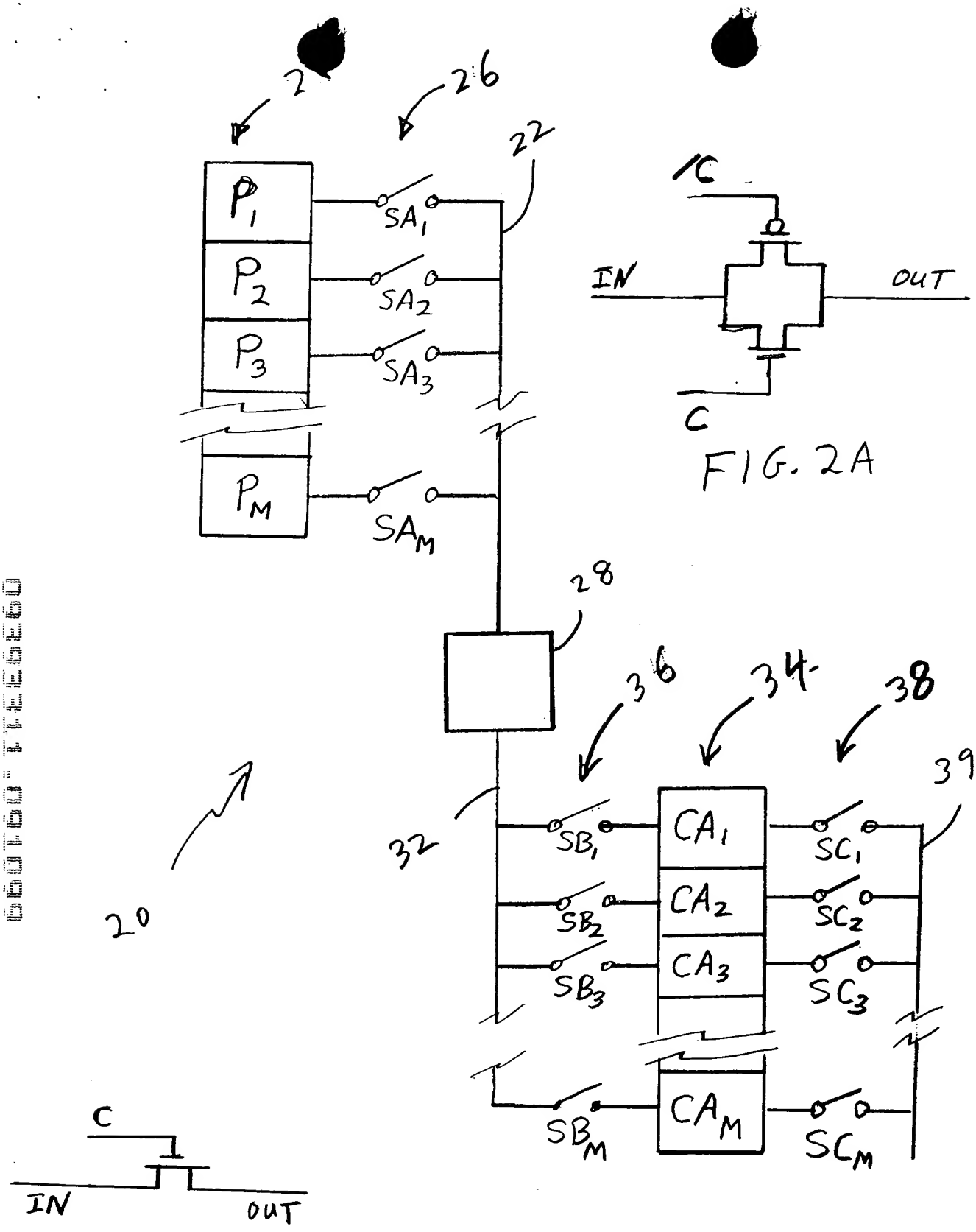
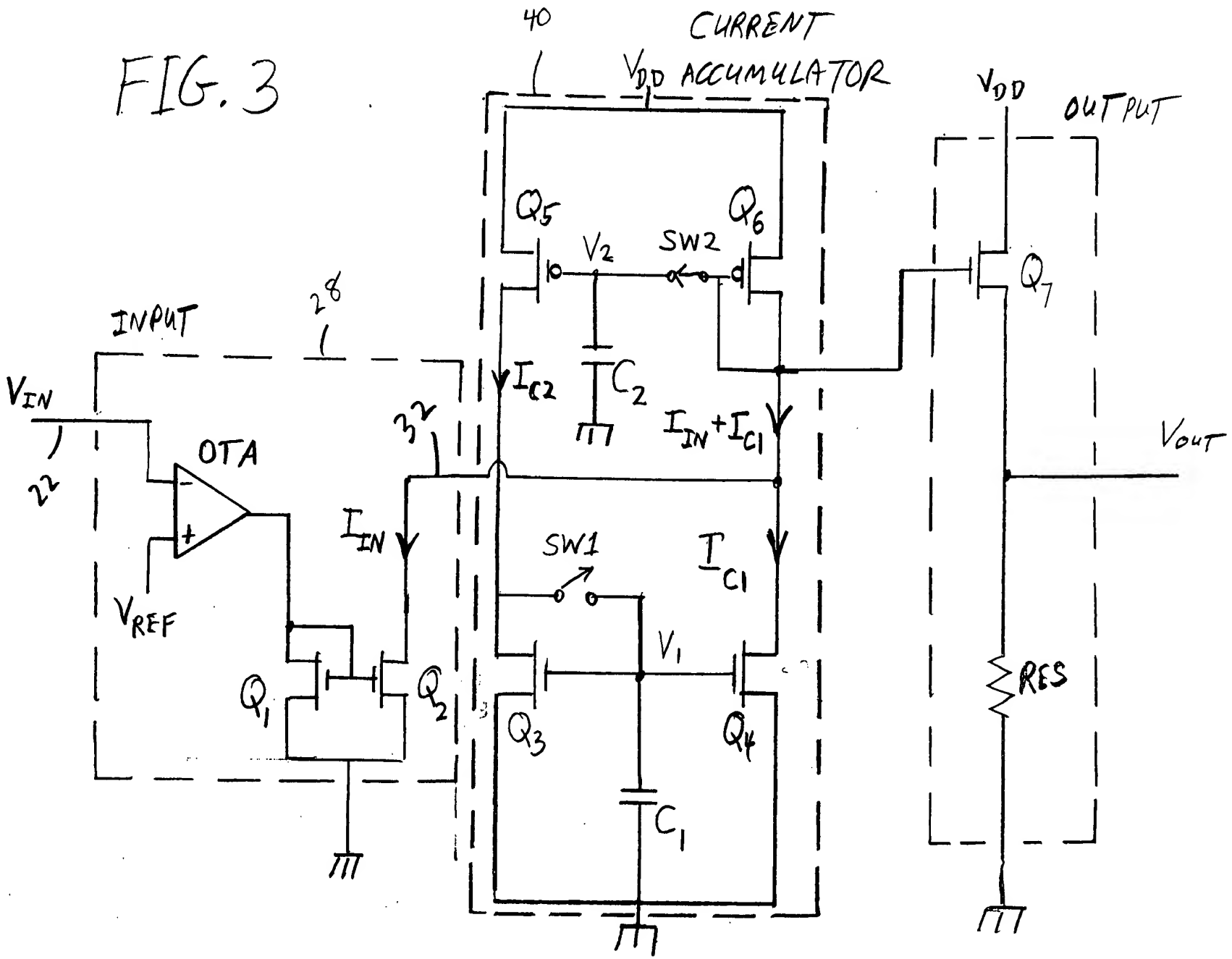


FIG. 2A

FIG. 2B

FIG. 2

FIG. 3



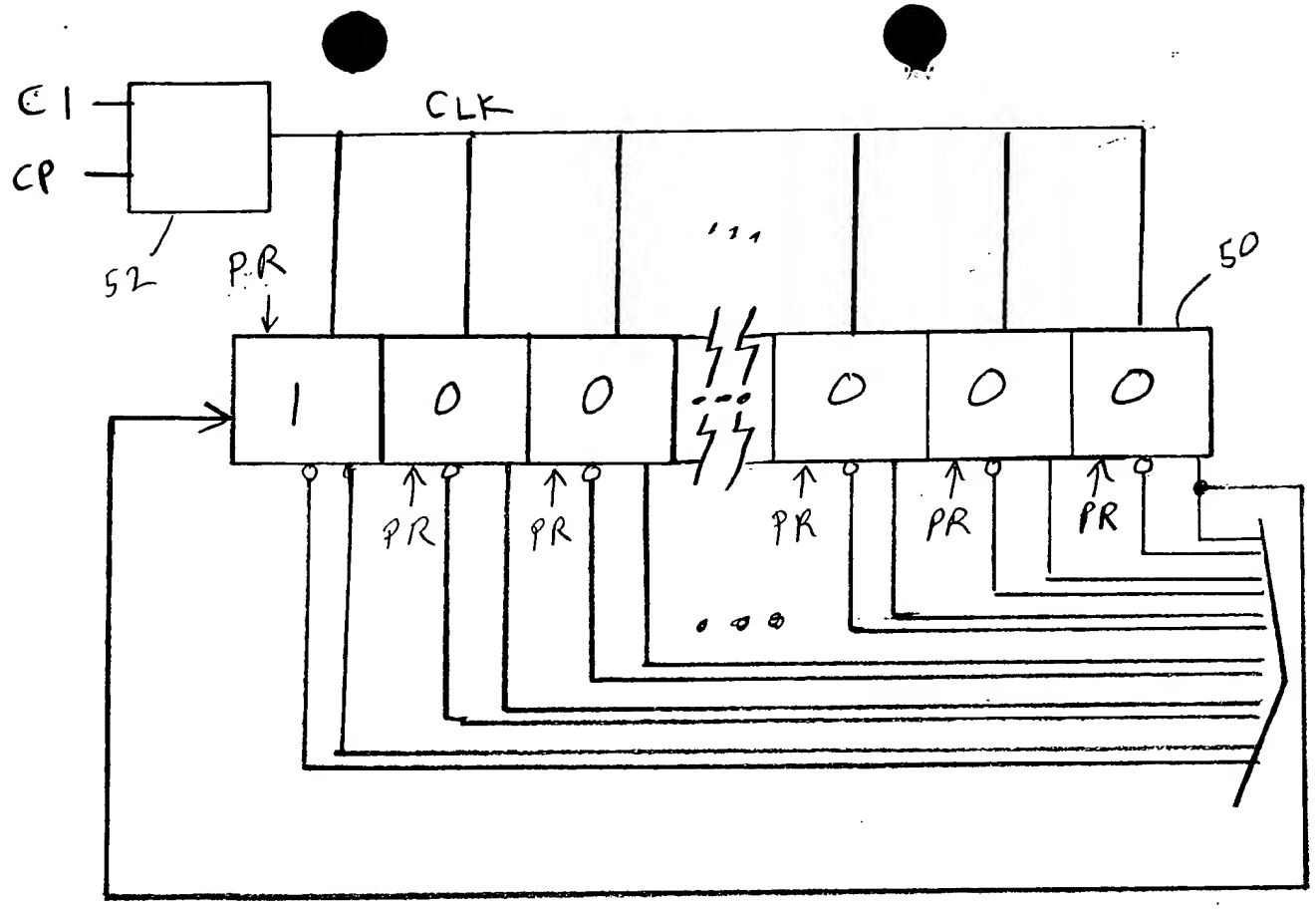
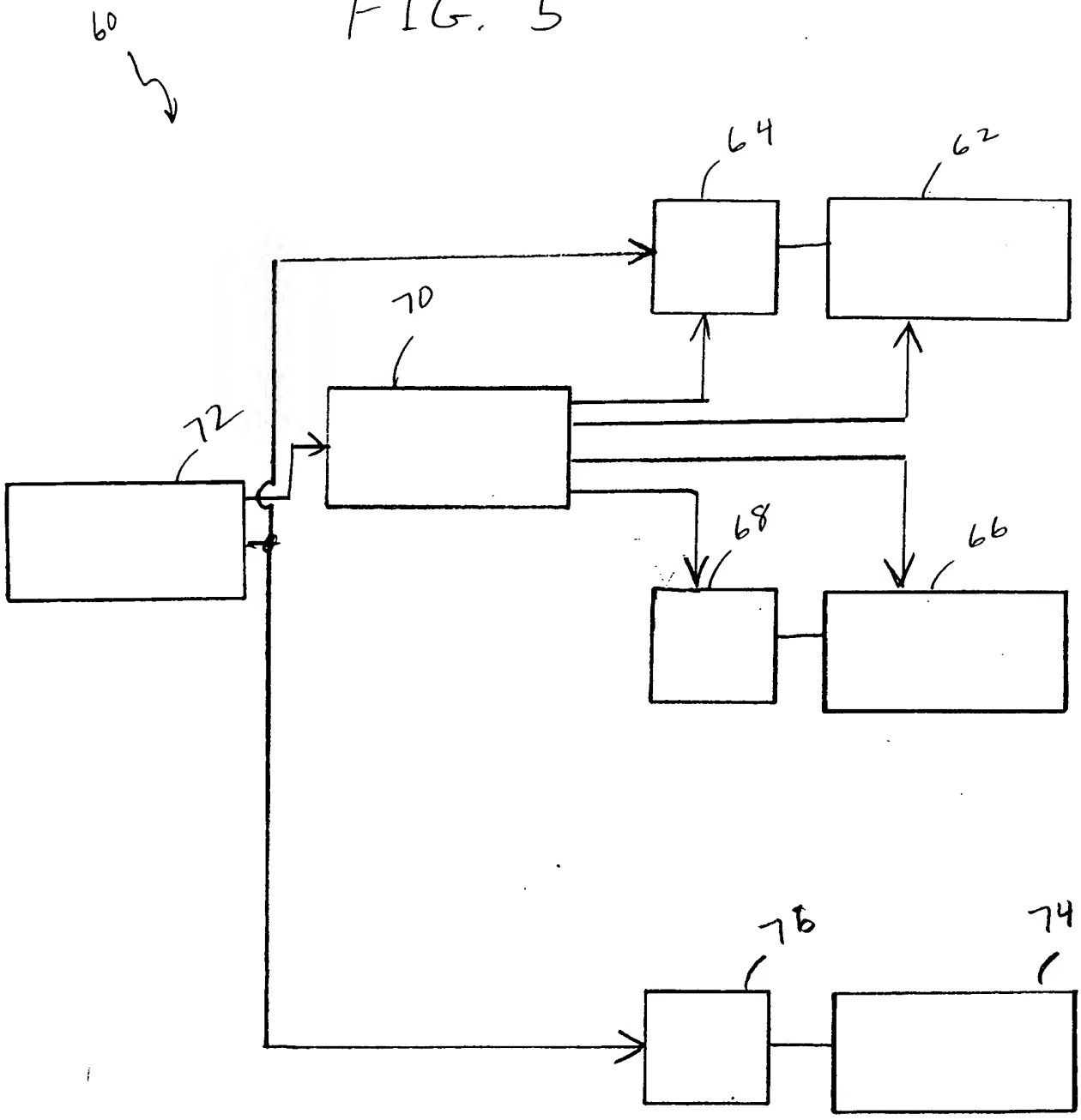


FIG 4

FIG. 5



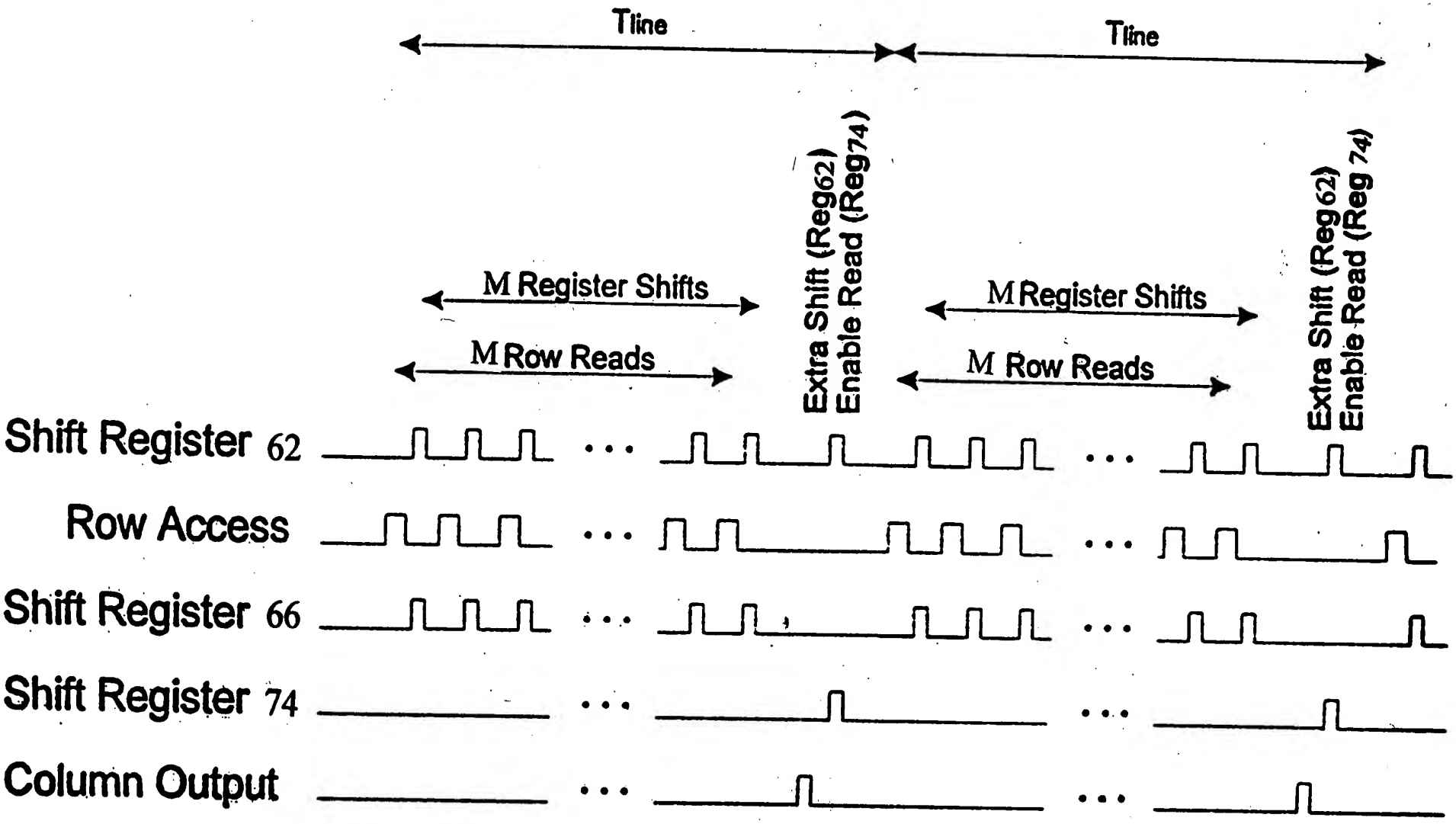
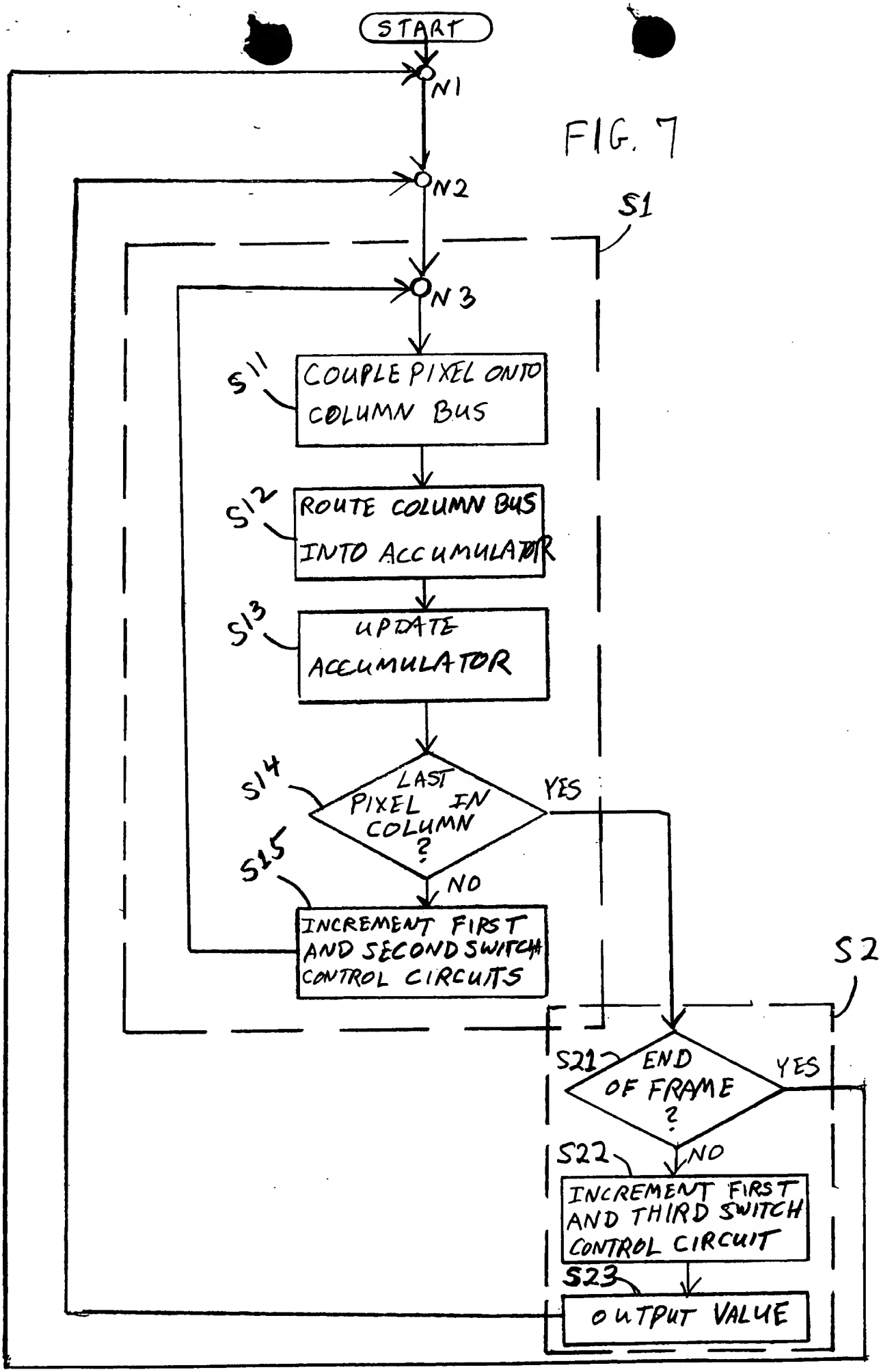

Tline
Tline
M Register Shifts
M Row Reads
Extra Shift (Reg62)
Enable Read (Reg74)
M Register Shifts
M Row Reads
Extra Shift (Reg62)
Enable Read (Reg 74)
Shift Register 62
Row Access
Shift Register 66
Shift Register 74
Column Output

FIG. 6

FIG. 7


START
N1
N2
S1
N3
S11
COUPLE PIXEL ONTO COLUMN BUS
S12
ROUTE COLUMN BUS INTO ACCUMULATOR
S13
UPDATE ACCUMULATOR
S14
LAST PIXEL IN COLUMN ?
YES
NO
S15
INCREMENT FIRST AND SECOND SWITCH CONTROL CIRCUITS
S2
S21
END OF FRAME ?
YES
NO
S22
INCREMENT FIRST AND THIRD SWITCH CONTROL CIRCUIT
S23
OUTPUT VALUE